

WHAT IS CLAIMED IS:

1. A programmable logic array integrated circuit comprising:

5 a two dimensional array of Logic Array Blocks ("LABs"), each comprising:

a memory block having a plurality of memory columns;

10 control means coupled to said memory block; and

an intra-LAB plurality of conductors being programmably coupled to said control means;

15 a first plurality of conductors, each of which extends along a first dimension of said two dimensional array, said first conductors selectively and programmably coupled to said intra-LAB plurality of conductors; and

20 a second plurality of conductors, each of which extends along a second dimension of said two dimensional array, said second conductors programmably coupled to said memory block and said first plurality of conductors.